

Appendix A
Changes to the Claims

The rewritten claims were revised as follows:

- 1 14. (amended) A single-chip integrated circuit for [controlling] monitoring an
2 optoelectronic device, comprising:
3 memory, including one or more memory arrays for storing information related to the
4 optoelectronic device;
5 analog to digital conversion circuitry for receiving a plurality of analog signals from
6 the optoelectronic device, the analog signals corresponding to operating conditions of the
7 optoelectronic device, converting the received analog signals into digital values, and storing
8 the digital values in predefined locations within the memory; and
9 a memory interface for reading from and writing to locations within the memory in
10 accordance with commands received from a host device.
- 1 39. (amended) A method of [controlling] monitoring an optoelectronic device,
2 comprising:
3 in accordance with instructions received from a host device, reading from and writing
4 to locations within a memory; and
5 receiving a plurality of analog signals from the optoelectronic device, the analog
6 signals corresponding to operating conditions of the optoelectronic device, converting the
7 received analog signals into digital values, and storing the digital values in predefined
8 locations within the memory;
9 wherein the method is performed by a single-chip controller integrated circuit.